

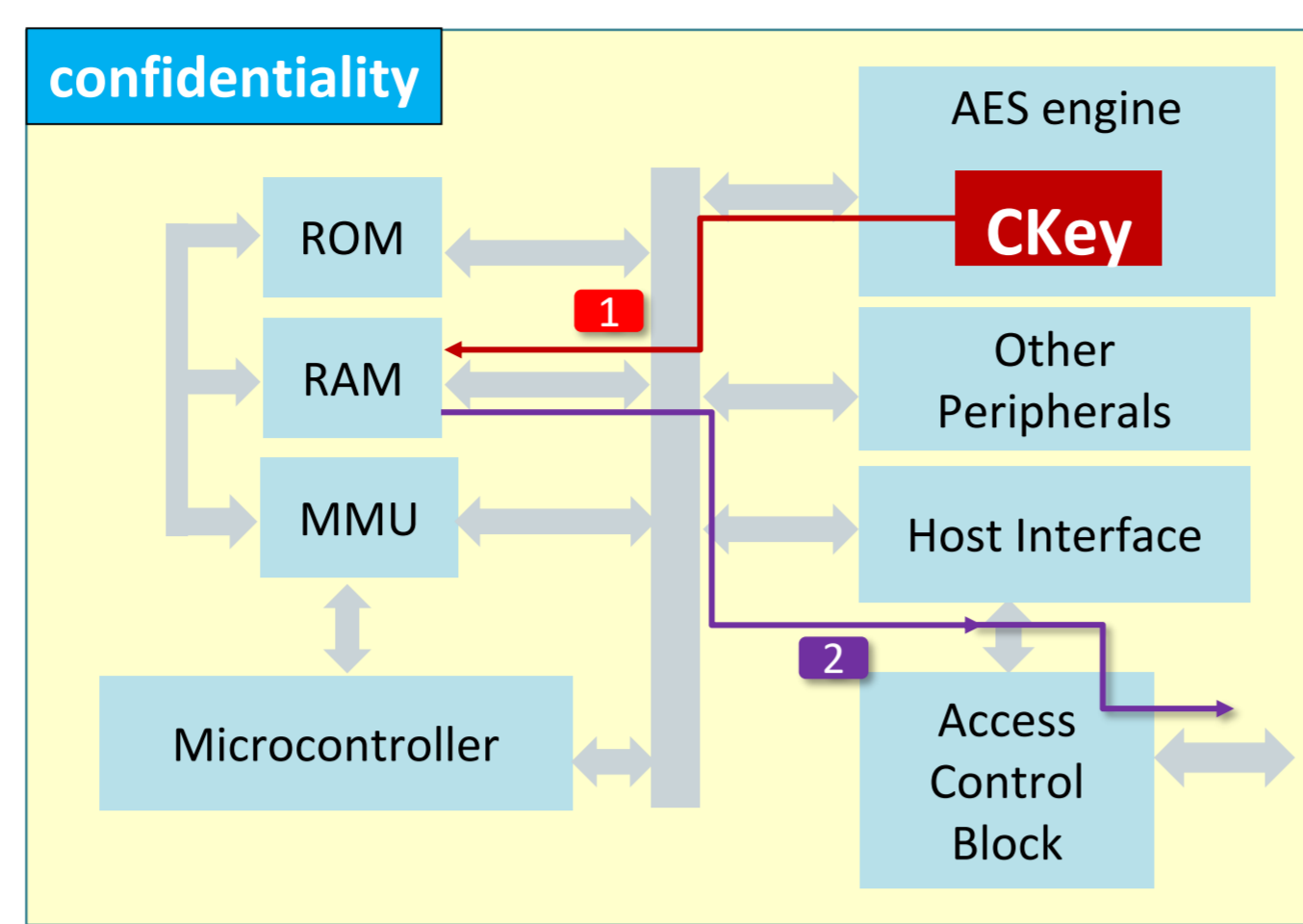
# TOWARDS SCALABLE SOC SECURITY VALIDATION

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## Introduction

- Confidentiality
  - secret information should not flow to untrusted region
- Integrity
  - no information flow from untrusted region to secure location

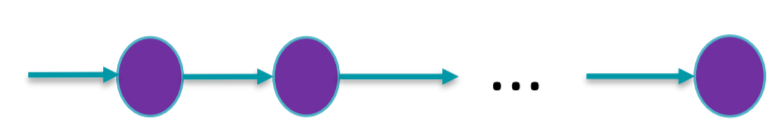


[Subramanyan, P., Arora, D., DATE'14]

Integrity is a *dual* of confidentiality

Confidentiality and integrity are 2-safety properties

- Properties refuted by observing **two** finite traces
- A **trace** is sequence of execution states,  $t = s_0 s_1 \dots s_n$



- 2-safety property is from the class of **Hyperproperties**

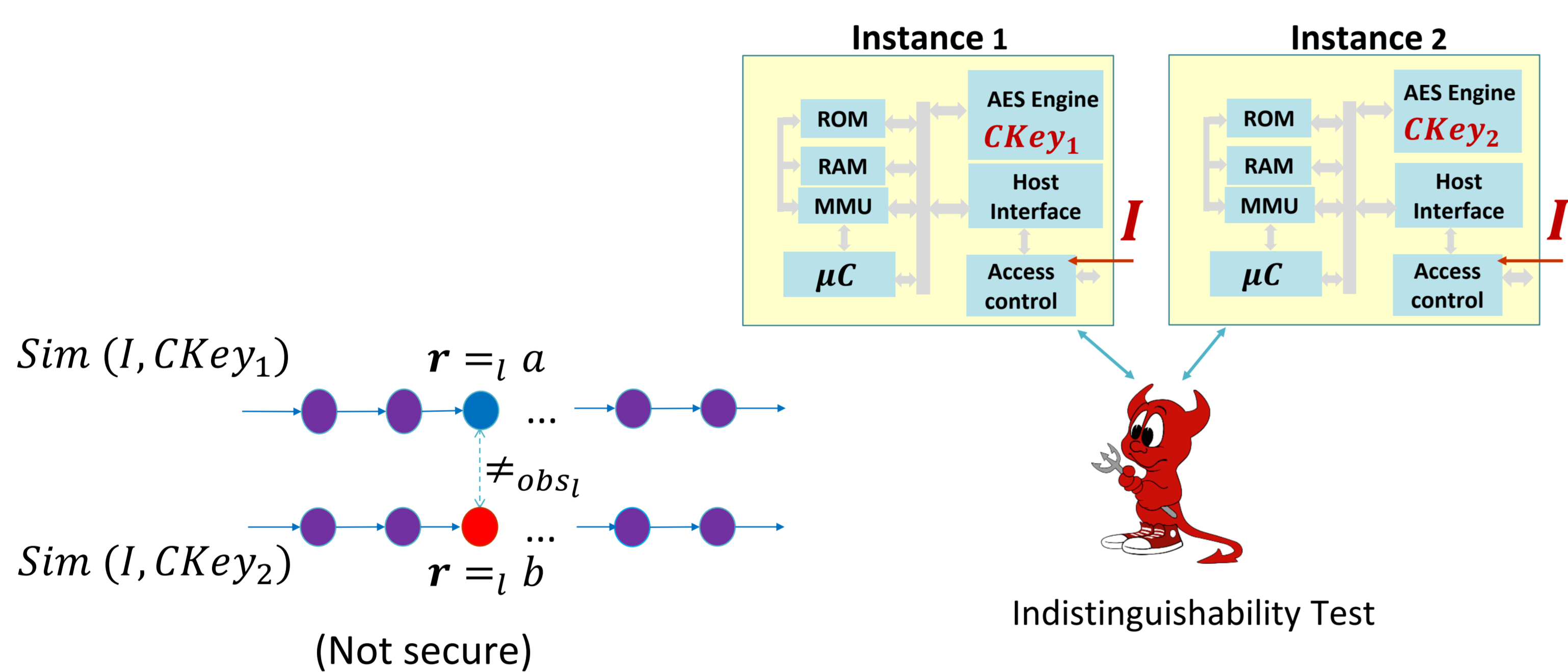
## Problem Statement

Proving confidentiality and integrity

show system leaks no secret information

or

show execution traces are indistinguishable to untrusted entity



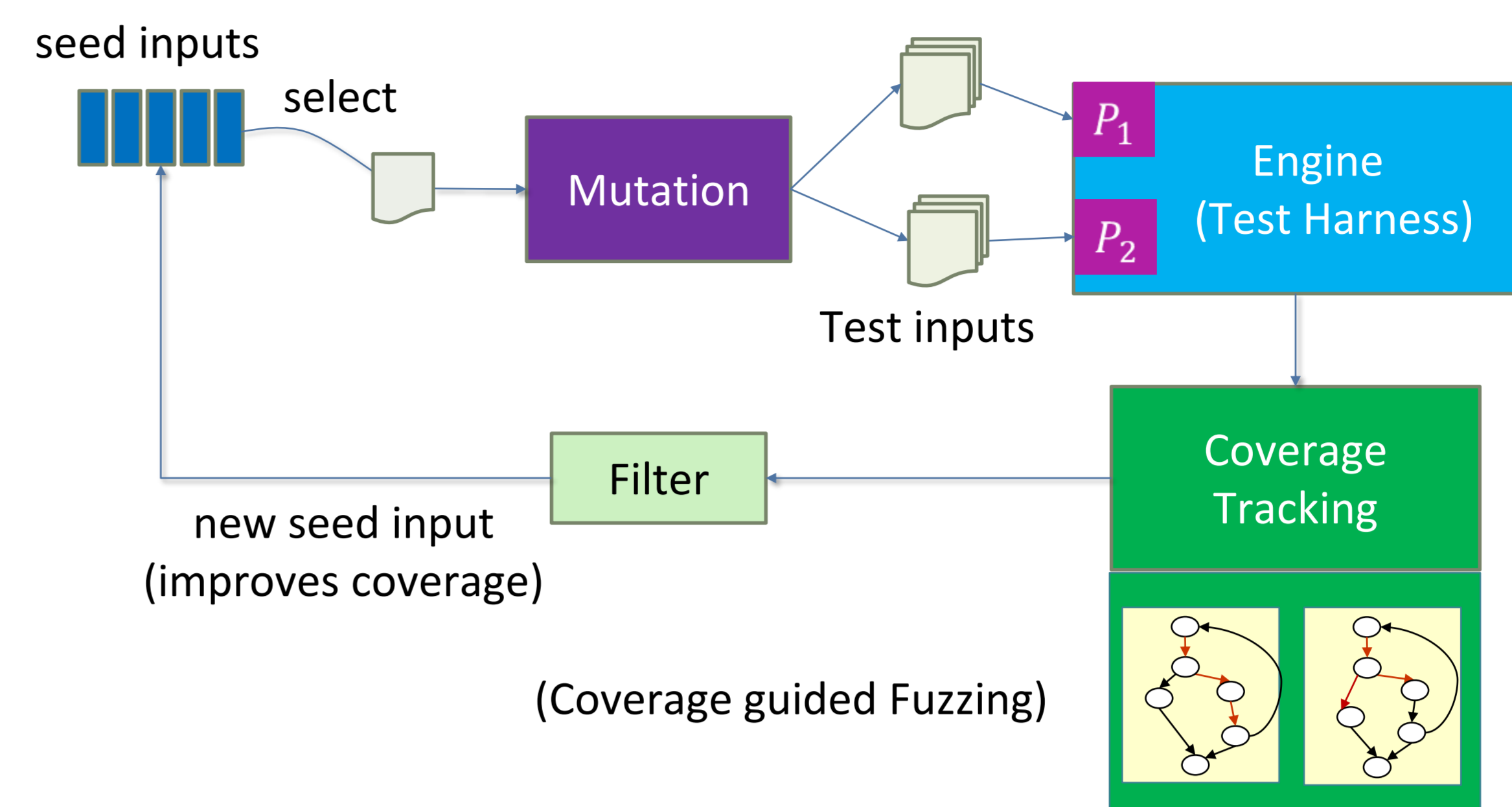
Goal :

- automated technique for finding 2-safety hyperproperty violations
- language for specifying security properties

## Prior Art

SPECIFICATION	Non-interference	<ul style="list-style-type: none"> <li>Goguen, J. A., &amp; Meseguer, J. Security policies and security models.</li> <li>Zdancewic, S., et al. Observational determinism for concurrent program.</li> <li>HyperLTL and HyperCTL* are extension of LTL and CTL* [CFK+14]</li> </ul>
	Observational Determinism	
VERIFICATION	HyperLTL HyperCTL*	<ul style="list-style-type: none"> <li>Hardware design is converted to a <b>GLIFT</b> logic for verification [HOI+11]</li> <li>SecVerilog, Caiosson and Sapper uses information flow type systems at HDL</li> </ul> <p><b>GLIFT : much overhead</b></p> <p><b>HDL LIFT : doesn't guarantee no violations in runtime</b></p>
	Information Flow Tracking (GLIFT, HDL LIFT)	
FUZZING	Model Checking	<ul style="list-style-type: none"> <li>Barthe, et al. "Secure information flow by self-composition."</li> <li>Terauchi, T., &amp; Aiken, A. "Secure information flow as a safety problem."</li> <li>Sousa, M., &amp; Dillig, I. "Cartesian hoare logic for verifying k-safety properties."</li> <li>Subramanyan, P., et al. "Verifying information flow using symbolic execution."</li> </ul> <p><b>computationally expensive → not scalable</b></p>
	American Fuzzy Lop (afl)	

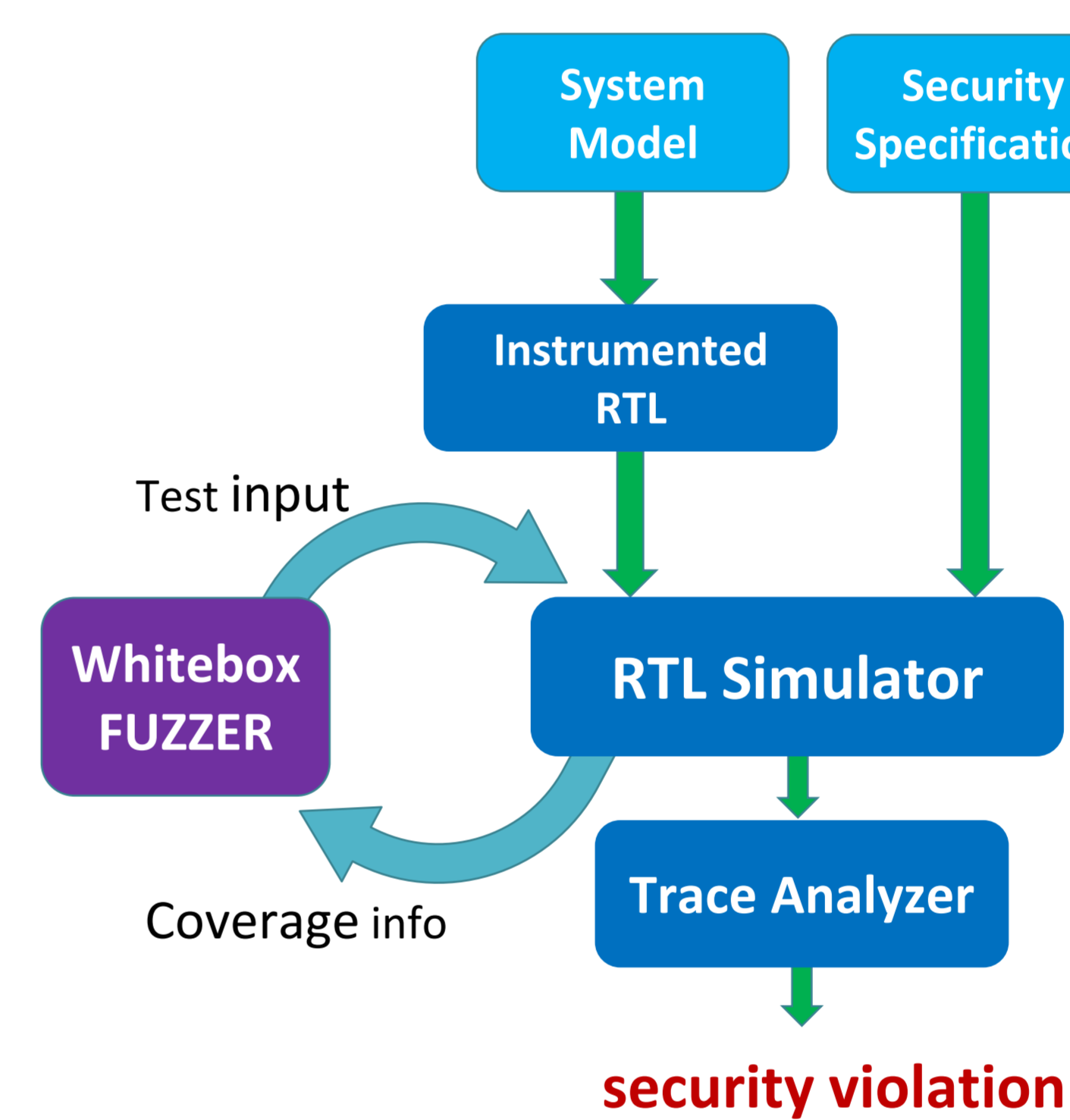
## Technical Challenges



(Coverage guided Fuzzing)

- Specification Language
- Mutation Algorithm
  - Need new algorithms to generate inputs that trigger 2-safety violations
- Test harness
  - Modify test harness engine to work with two system instances
- Coverage tracking
  - Mechanism to find new seed inputs
  - Ensures fuzzer does not revisit inputs
  - Need new coverage metrics to explore product state space

## Proposed System Architecture



Hardware modeling

- Verilog implementation

Intermediate representation

- Instrument Verilog model to collect simulation metric

Fuzzer

- A variant of AFL to be used along with Hyperproperties

## Conclusion

- Existing solutions for SoC security validation are not effective
- Fuzzing has the potential to be scalable.
- It has been successful in finding many software vulnerabilities.
- HYPERFUZZING leverages power of fuzzing to find security violations in SoCs.

## References

- Godefroid, Patrice, Michael Y. Levin, and David Molnar. "SAGE: whitebox fuzzing for security testing." Communications of the ACM 55.3 (2012): 40-44.
- Bounimova, Ella, Patrice Godefroid, and David Molnar. "Billions and billions of constraints: Whitebox fuzz testing in production." Proceedings of the 2013 International Conference on Software Engineering. IEEE Press, 2013.
- Clarkson, Michael R., and Fred B. Schneider. "Hyperproperties." Journal of Computer Security 18.6 (2010): 1157-1210.
- Barthe, Gilles, Pedro R. D'Argenio, and Tamara Rezk. "Secure information flow by self-composition." Proceedings. 17th IEEE Computer Security Foundations Workshop, 2004.. IEEE, 2004.
- Terauchi, Tachio, and Alex Aiken. "Secure information flow as a safety problem." International Static Analysis Symposium. Springer, Berlin, Heidelberg, 2005.