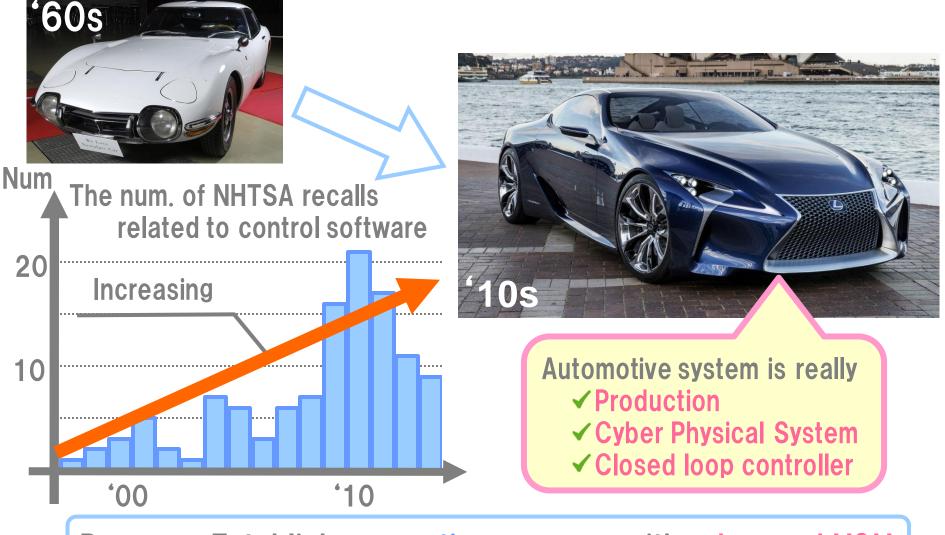
# Combining Requirement Mining, Software Model Checking and Simulation-Based Verification for Industrial Automotive Systems

2016/10/06 Tomoya Yamaguchi and Tomoyuki Kaga TOYOTA MOTOR CORPORATION Alexandre Donzé and Sanjit A. Seshia University of California, Berkeley

## **TOYOTA V&V** Perspective

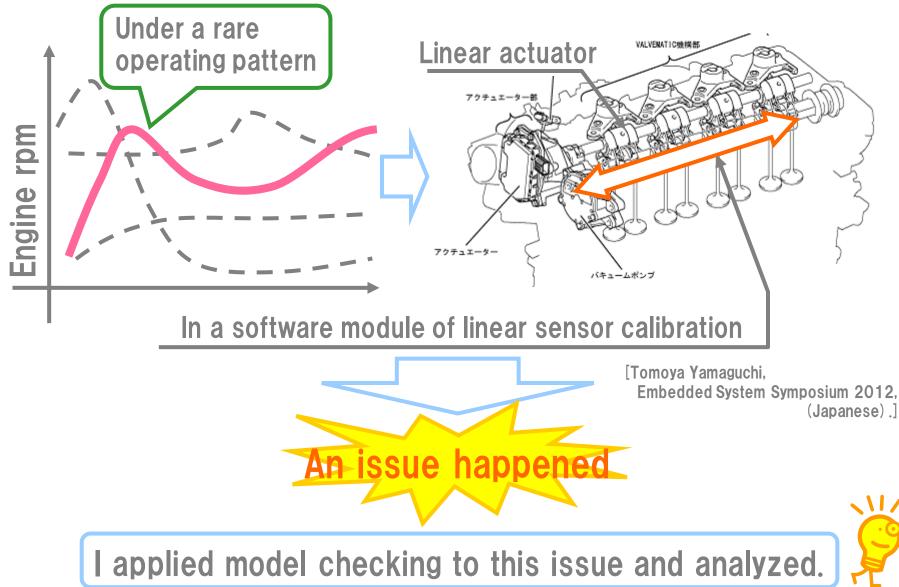
#### Automobile system becomes more complex and larger in scale.



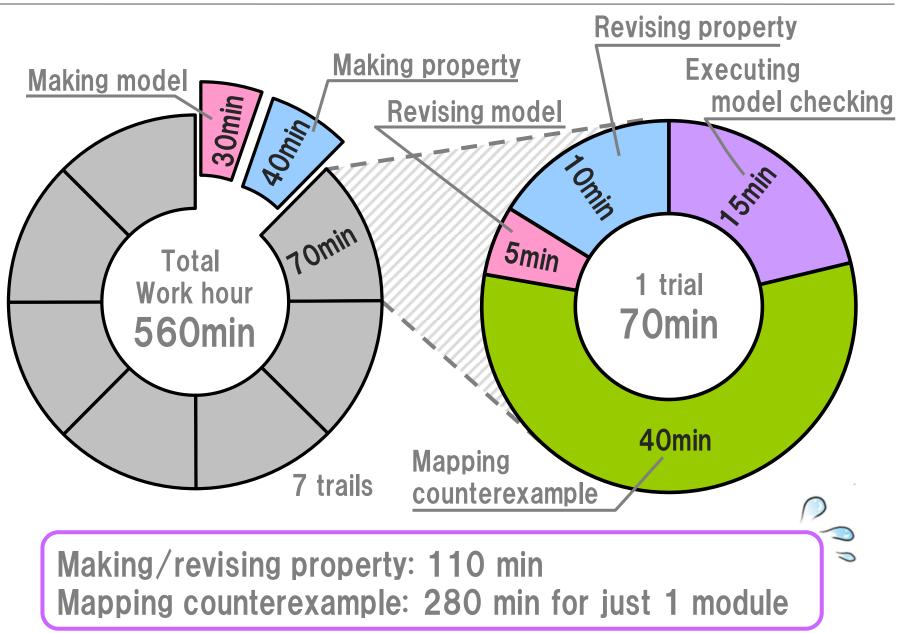
Purpose: Establish prevention process with advanced V&V

## **Applying model checking to our CPS**

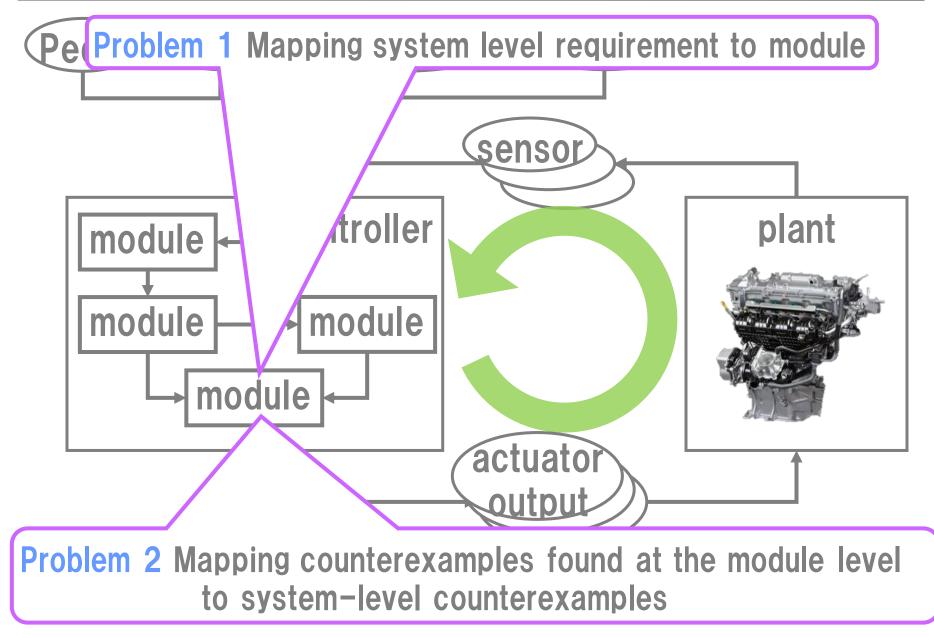
An issue occurred when we were developing.



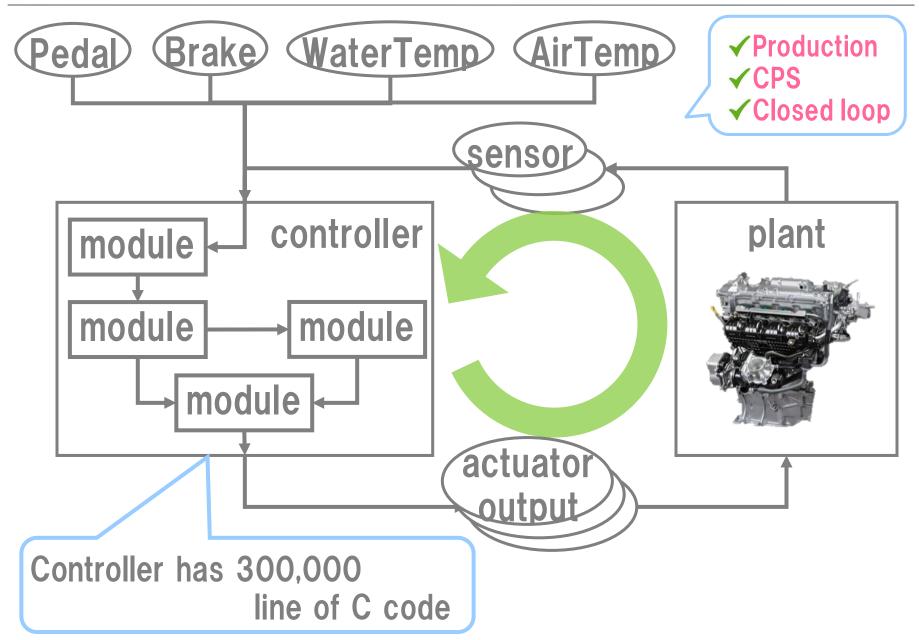
## **Applying model checking to our CPS**



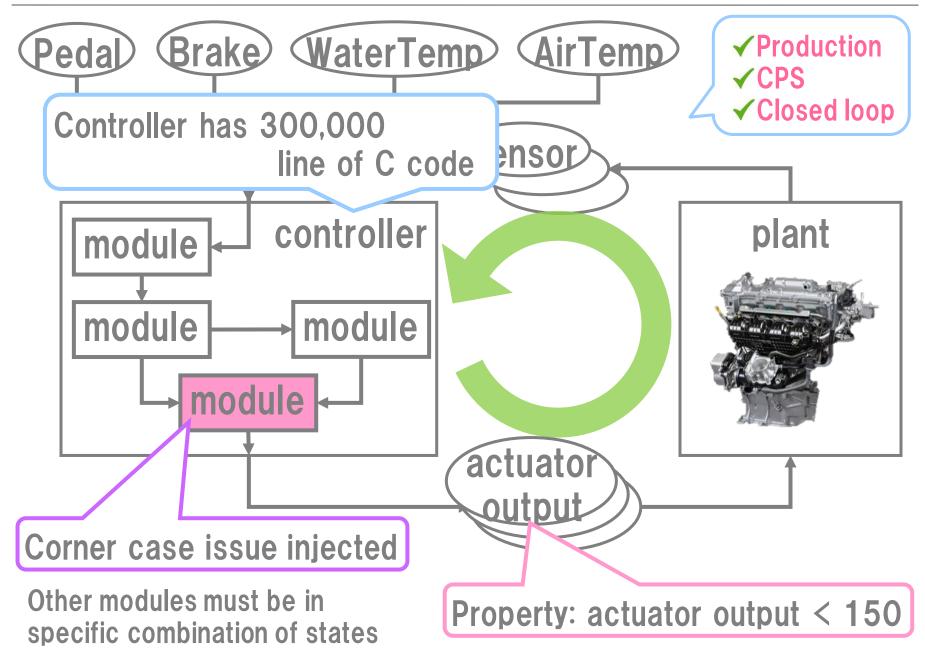
## The problem of applying model checking



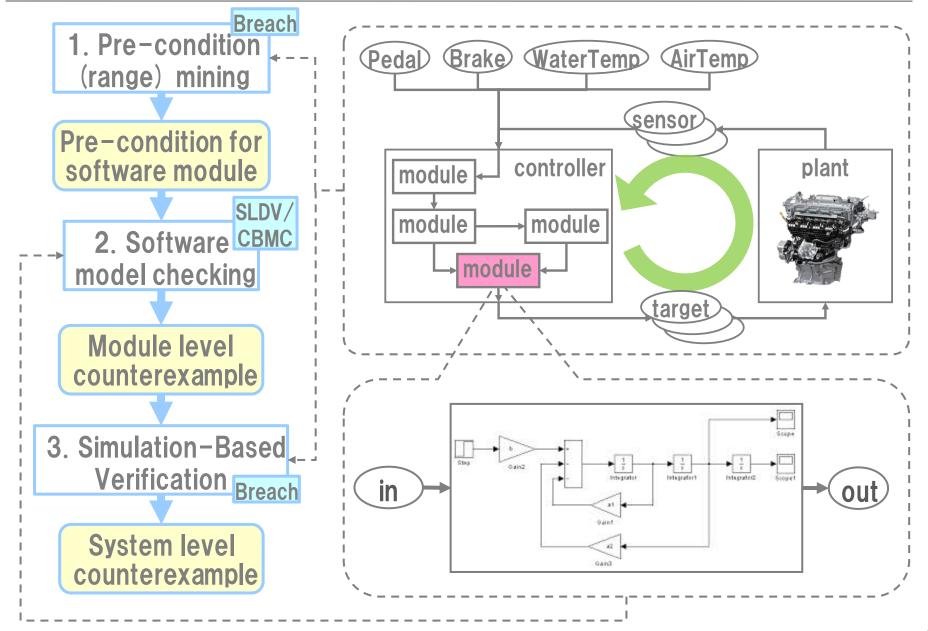
## **V&V** object: Injected issue on actual Engine SILS



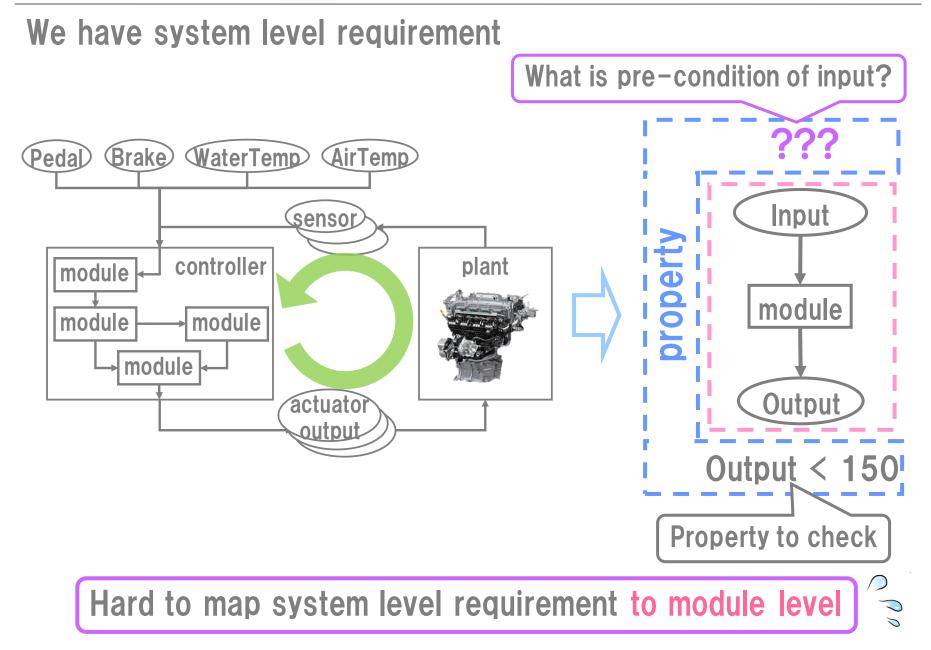
## **V&V** object: Injected issue on actual Engine SILS



## **Overview of our methodology**

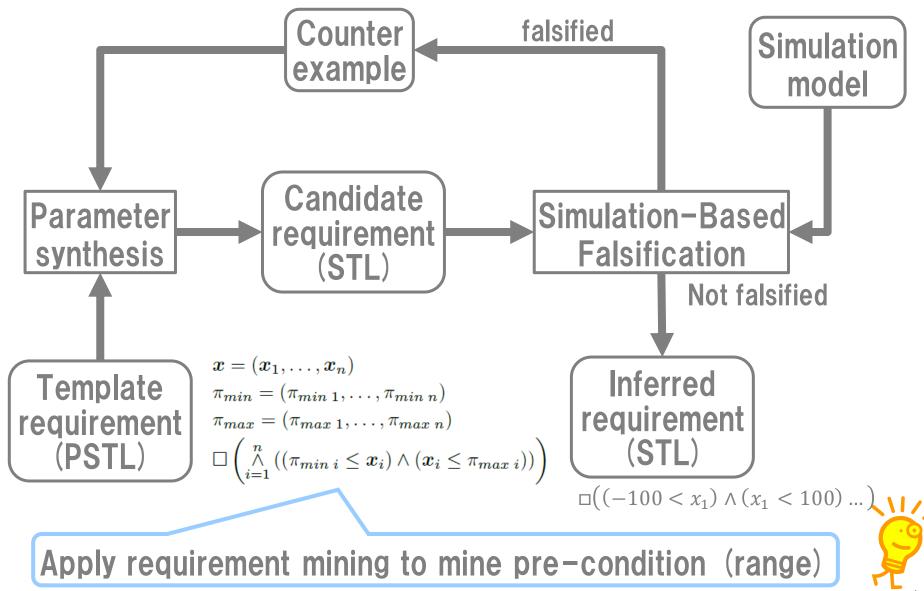


### Problem.1 Mapping system level requirement to module



### **Counter measure for problem 1:Requirement Mining**

[Xiaoqing Jin, et al., HSCC 2013]



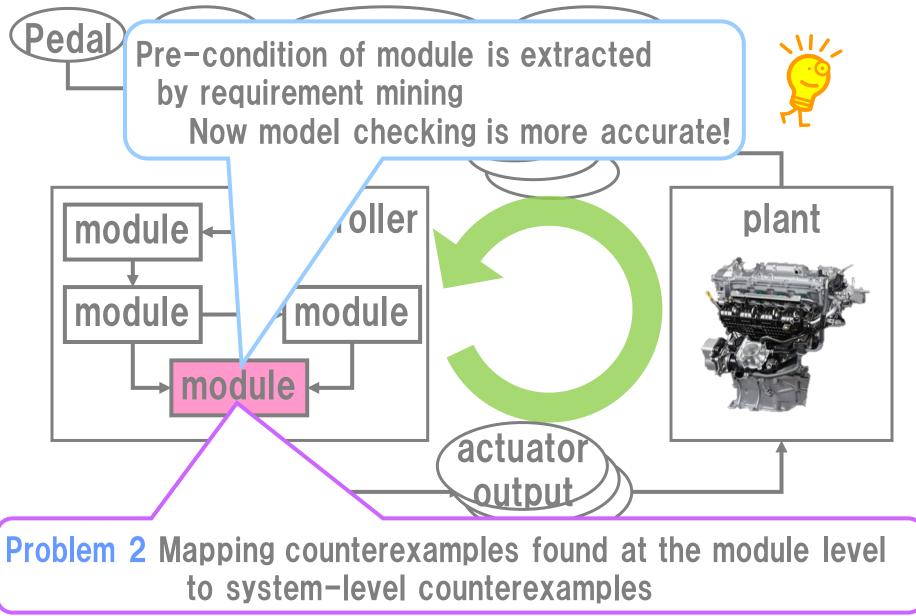
### **Result of using module level requirement**

#### Counterexample comes from model checking

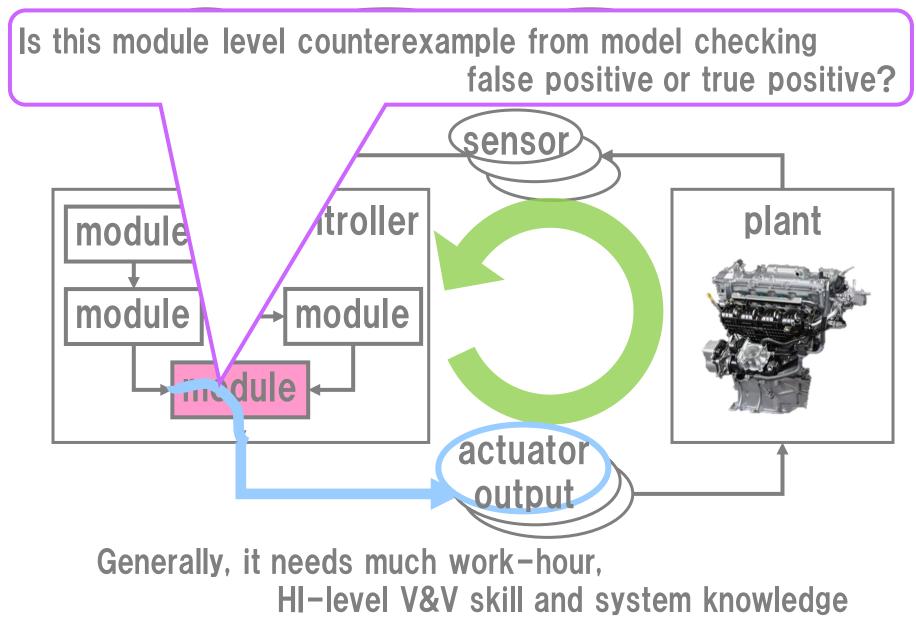
No range	With range mining	
counterexample	range	counterexample
89.4	[-30.0, 100.0]	90.0
3.5	[0.0, 1.0]	1.0
5	[0, 6]	6
0	0	0
0	[0, 1]	0
1	[0, 1]	1
0	[0, 1]	0
2600.0	[0.0, 5310.9]	2600.0
	counterexample         89.4         3.5         5         0         0         1         0	counterexample         range           89.4         [-30.0, 100.0]           3.5         [0.0, 1.0]           5         [0, 6]           0         0           0         [0, 1]           1         [0, 1]           0         [0, 1]

false positive case is avoided by using range mining

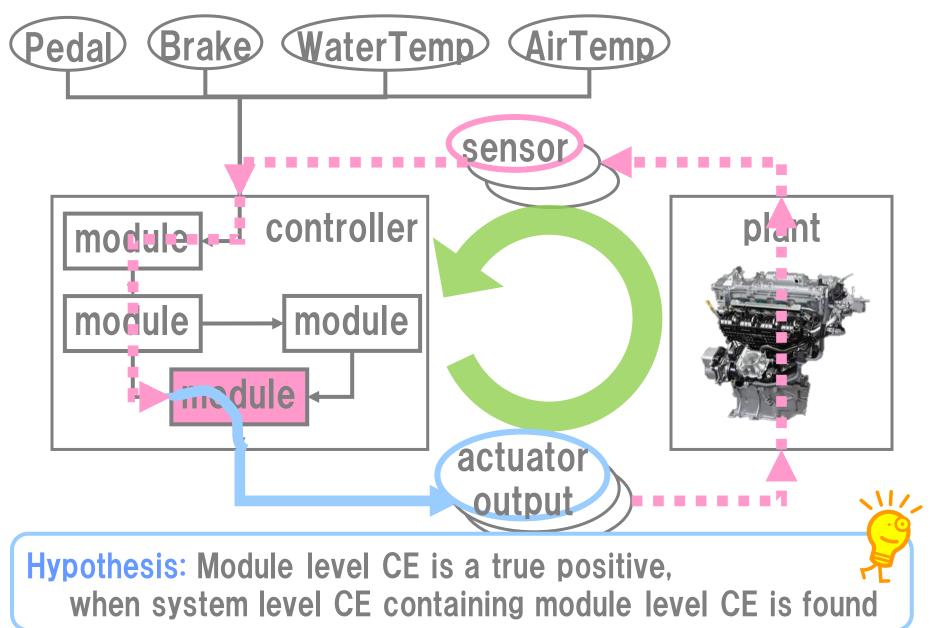
#### Problem 2 Mapping counterexamples found at the module level to system-level counterexamples



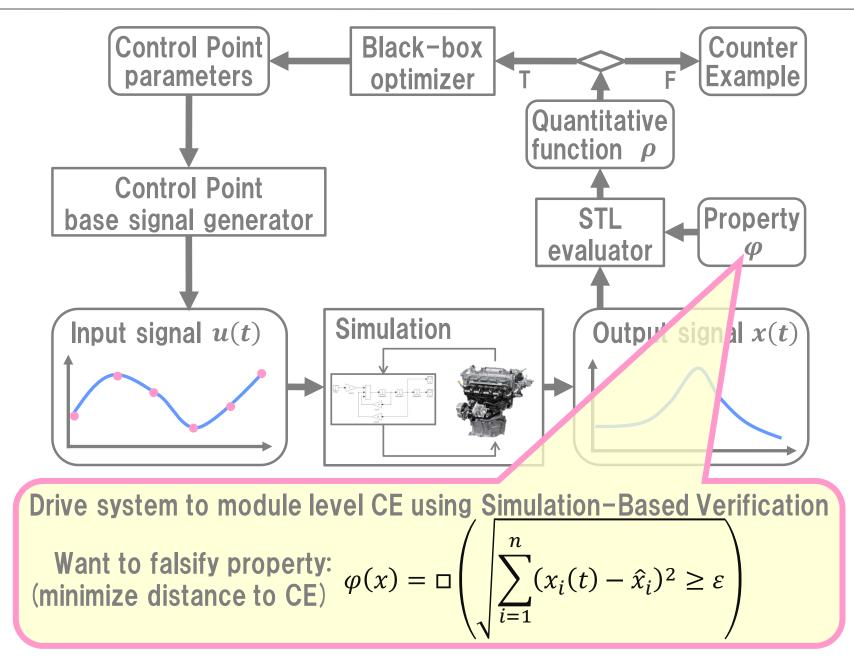
#### Problem 2 Mapping counterexamples found at the module level to system-level counterexamples



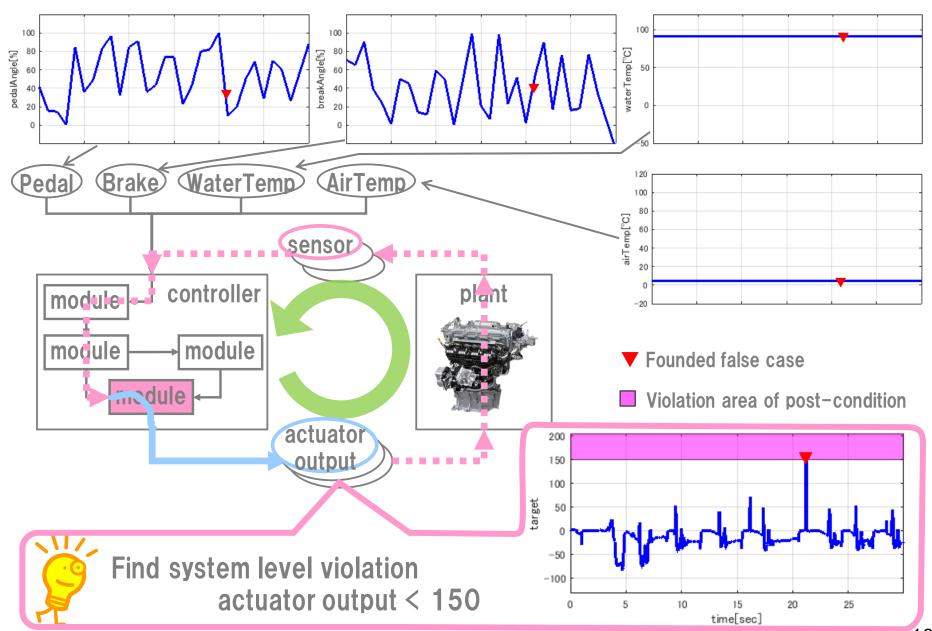
#### Problem 2 Mapping counterexamples found at the module level to system-level counterexamples



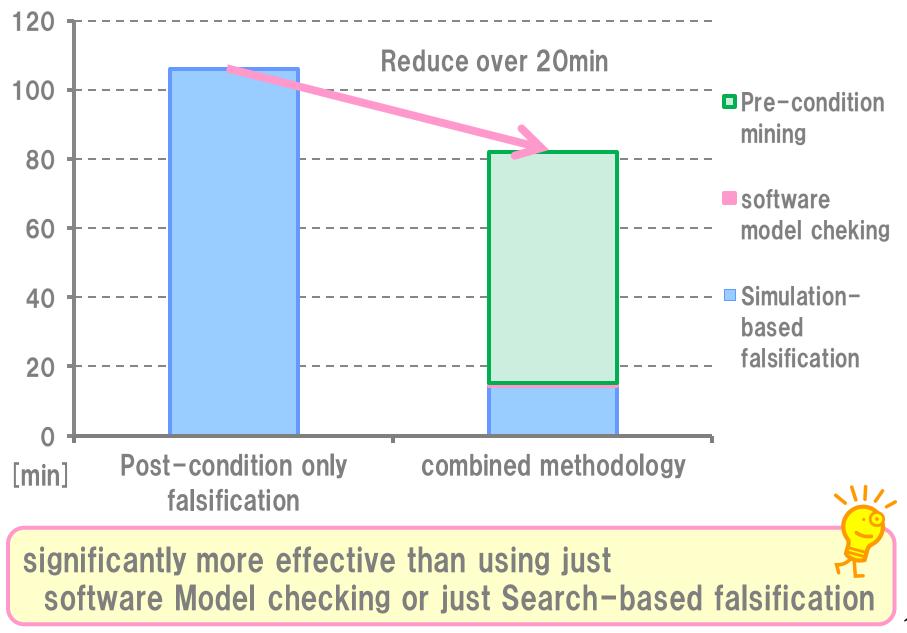
## Simulation-Based Verification with cost function



## Found system level corner case issue



### **Comparison with just Simulation-based Falsification**



## Conclusion

- We propose combined methodology

   (= Requirement Mining + Model Checking
   + Simulation-based verification)
- New methodology is applied to production closed loop CPS
- Our combined methodology can be significantly more effective than using just software Model checking or just Simulation-based verification

## **Special thanks**

- **Breach:** Breach is provided by U.C. Berkeley, Prof. Sanjit Seshia and Dr. Alexandre Donzé. Breach has flexible extendibility for the requirement mining and the simulated-base verification.
- **CBMC:** CBMC is provided by Univ. Oxford, Prof. Daniel Kroening and Dr. Martin Brain. CBMC is a sophisticated tool and was greatly helpful for our case study.
- SMiL: Toyota in-house engine SILS. Fujitsu-ten provides and also supports us well.